#### (19)日本国特許庁 (JP)

HO1L 21/66

## (12) 公開特許公報(A)

(11)特許出願公開番号

## 特開平11-51970

(43)公開日 平成11年(1999)2月26日

(51) Int.Cl. <sup>6</sup>		識別記号	F I
C01P	1 /067		C 0 1 P

G 0 1 R 1/067 H 0 1 L 21/66 В

審査請求 有 請求項の数4 OL (全 5 頁)

(21)出願番号 特願平9-205709 (71)出願人 000004237

(22)出願日 平成9年(1997)7月31日

日本電気株式会社

東京都港区芝五丁目7番1号

(72)発明者 福島 善彦

東京都港区芝五丁目7番1号 日本電気株

式会社内

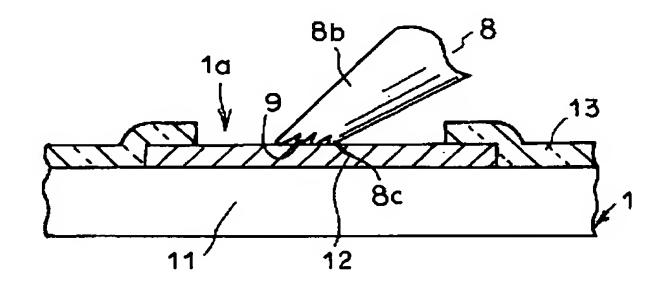
(74)代理人 弁理士 鈴木 章夫

### (54) [発明の名称] プロープカード

### (57)【要約】

【課題】 半導体集積回路装置のパッドに電気接触して テスタによる測定を行うためのプローブ針における、接 触抵抗の低減と、良好な電気接触を確保する。

【解決手段】 半導体集積回路装置1に設けられたパッド1aに対して電気接触され、前記半導体集積回路装置とテスタとを電気接続するためのプローブ針8を備えるプローブカードにおいて、前記プローブ針8の先端部8bが複数の突部9で構成され。この突部9は、先端が鋭角な三角状の断面形状をした格子状の突部、あるいは、先端が鋭角な三角状の断面形状をした錘型の突部とし構成される。複数の突部9のそれぞれにおいてパッド1aに電気接触されるため、接触面積を増大して、接触抵抗を低減することが可能となる。また、複数の突部9がそれぞれパッド1aの表面の酸化膜をつき破るため、好適な電気接触が可能となる。



1 : I C

1a:パッド

11:基板

12:導電膜

13:保護膜

8:プローブ針

9:突部

1

#### 【特許請求の範囲】

14

【請求項1】 半導体集積回路装置に設けられたパッド に対して電気接触され、前記半導体集積回路装置とテスタとを電気接続するためのプローブ針を備えるプローブ カードにおいて、前記プローブ針の先端部が複数の突部で構成されていることを特徴とするプローブカード。

【請求項2】 前記突部は、先端が鋭角な三角状の断面 形状をした格子状の突部である請求項1に記載のプロー ブカード。

【請求項3】 前記突部は、先端が鋭角な三角状の断面 10 形状をした錘型の突部である請求項1に記載のプローブカード。

【請求項4】 前記突部は断面形状が鋸歯状である請求項2または3に記載のプローブカード。

#### 【発明の詳細な説明】

## [0001]

【発明の属する技術分野】本発明は、半導体装置をウェハ状態で評価する際に用いるプローブカードに関し、特にウェハのボンディングパッドに接触するプローブ針の 先端部の形状の改良に係るものである。

#### [0002]

【従来の技術】一般に、半導体集積回路装置(IC)の製造過程での半導体ウェハ上に形成される半導体装置の電気的特性等を測定するために、いわゆるテスタと呼ばれる検査装置が用いられる。このテスタでは、ICのパッド(電極)と、テスタ本体を電気的に接続するためのボードとしてのプローブカードが設けられており、このプローブカードに備えれらるニードル、すなわちプローブ針をICのパッドに接触してICに対する電気接続を行うように構成される。このプローブカードは、前記し30たプローブ針と、複数本のプローブ針を固定支持する固定ボードとで構成されている。

【0003】従来のプローブカードにおけるプローブ針 先端部の拡大図及びICのパッドとの接触状態の断面図 をそれぞれ図4(a),(b)に示す。従来のプローブ カードは、プローブ針8の先端部8bが尖っており、ま た、円錐状の形状をしている。そして、IC1のシリコ ン基板11の表面に形成されて保護膜13で周囲を覆わ れた導電膜12からなるパッド1aの表面にそのプロー ブ針8の先端部8bが接触され、このパッド1aを通し で電気接続が行われる。このようなプローブカードとし ては、例えば、特開昭62-295426号公報、特開 平4-199836号公報に記載のものがある。

【0004】しかしながら、このようなプローブカードでは、プローブ針8の先端部8bが尖っているために、パッド1aとの接触面積が小さく、接触抵抗が大きくなり易い。このため、微弱電流を通流するような場合には、その接触抵抗によって電流が低下され、好適な測定が難しくなるという問題がある。このため、従来では、図5(a)または図6(a)に示すように、プローブ針 50

8,8Aの先端を平面に形成し、この平面状の先端面8cをパッド1aに接触させる構成が提案されている。このプローブ針8,8Aでは、図5(b)または図6

(b) に示すように、パッド1aとプローブ針8との接触面積が増大でき、前記した接触抵抗を低減することが可能となる。このようなプローブカードとしては、例えば、特開昭62-295426号公報、特開平4-199836号公報に記載のものがある。

#### [0005]

10 【発明が解決しようとする課題】しかしながら、このようにプローブ針の先端を平面に形成した構成では、プローブ針8,8 Aが正しい角度でパッド1 a の表面に接触したときには良いが、その角度にずれが生じたような場合には、プローブ針8,8 Aの先端面8 c が正しくパッド1 a の表面に接触しなくなり、結果として両者が線または点接触状態となり、前記した接触面積の増大と接触抵抗の低減の各効果を得ることができない場合がある。また、一般にこの種のプローブ針は、パッド表面に発生する100 Å程度の薄い酸化膜をつき破って電気接続を20 行っており、そのためにプローブ針の先端を尖らせる構成ともされているが、前記したように平面に形成したときには、この薄い酸化膜をつき破ることができなくなり、結果として好適な電気接触ができなくなるという問題が生じることになる。

【0006】本発明の目的は、プローブ針とパッドとの接触抵抗を低減し、両者の好適な電気接触を可能にして、測定の正確性が向上し、テストの信頼性を向上することを可能にしたプローブカードを提供することにある。

### 0 [0007]

【課題を解決するための手段】本発明のプローブカードは、ICのパッドに対して電気接触されるプローブ針の 先端部が複数の突部で構成されていることを特徴とす る。ここで、前記突部は、先端が鋭角な三角状の断面形 状をした格子状の突部として、あるいは、先端が鋭角な 三角状の断面形状をした錘型の突部として構成される。 また、各突部は、その断面形状が鋸歯状であることが好ましい。

【0008】複数の突部のそれぞれにおいてパッドに電気接触されるため、接触面積を増大して、接触抵抗を低減することか可能となる。また、複数の突部がそれぞれパッド表面の酸化膜をつき破るため、好適な電気接触が可能となる。

#### [0009]

【発明の実施の形態】次に、本発明の実施形態について 図面を参照して説明する。図1は本発明のプローブカー ドを備えるテスタの主要部の概略構成を示す図である。 ステージ2にはテストされるIC、ここではシリコンウェハ1が載置され、前記ステージ2の下側に設けられた 上下駆動機構3によって上下移動可能に支持される。ま 14)

3

た、前記ステージ2の上方には固定ボード4が水平に配置され、図外のテスト機構に支持されている。前記固定ボード4にはプローブカード5が支持される。このプローブカード5は、絶縁基板6に矩形窓6aが開口されるとともに、その表面には所要の回路パターン7が形成されており、図外のテスト回路に電気接続されている。そして、前記矩形窓6a内には多数本のプローブ針8が配設されており、各プローブ針8はその基端部8aが前記矩形窓6aを通して下方に向けて突出されている。このため、ステージ2を上昇させることにより、IC1に設けられているパッド1aに前記各プローブ針8の先端部8bを接触させ、このプローブ針8及び回路パターン7を介して前記IC1とテスト回路とを電気接続し、所要のテストが実行されるように構成される。

【0010】図2(a)は前記プローブ針8の先端部8 bの拡大斜視図である。このプローブ針8の先端部8b は全体形状が円錐型に形成されているが、その先端面8 cはプローブ針の軸方向に対して傾斜した状態の面に形成されるとともに、この先端面には先端が鋭角で断面形状が三角形をした複数の突部9が格子状に配列形成されている。あるいは、図2(b)に示すように、前記先端面8cには、先端が鋭角な複数個の微小な円錐型の突起9Aを規則的にあるいは不規則に配列した構成としてもよい。なお、ここでは前記先端面8cは直径は80 $\mu$ Φ以下の円形ないし楕円形として形成されている。また、図2(b)の突部9Aは、角錘型であってもよい。

【0011】図3は前記したプローブカード5により I C1のテストを行っている状態の要部の拡大断面図であ る。前記IC1は、シリコンウェハの基板11の表面に 30 形成された所要パターンの導電膜12は保護膜13によ って被覆されており、この保護膜13に設けられた開口 に露呈されて前記導電膜12によって前記パッド1aが 形成されている。そして、図1に示したように、ステー ジ2が上昇されてプローブ針8の先端にパッド1aが当 接されることにより、図3に示したようなプローブ針8 とパッド1aとの電気接触が行われる。このとき、プロ ーブ針8の先端面8 c には、先端が鋭角な突部9が配設 されているため、これらの突部9の先端においてパッド 1 a の表面に発生している図外の酸化膜をつき破り、そ 40 の上でパッド1aの導電膜12に電気接触されることに なる。したがって、プローブ針8とパッド1 aとは、複 数の突部9においてそれぞれ電気接触されるため、1つ の尖った先端部で接触される従来構成に比較して接触面 積を増大し、接触抵抗を低減することが可能となる。ま た、その一方で複数の突部9の各先端でパッド表面の酸 化膜をつき破るため、良好な電気接触を得ることができ る。

【0012】特に、図2(a)の構成のプローブ針8では、突部9が格子状に形成されているため、各突部9と 50

パッド1aは線接触状態とされるため、図2(b)の構成のそれぞれが独立した突起状の突部9Aの構成よりも接触抵抗を低減することができる。また、一方で、図2(b)の構成では、各突部9Aがそれぞれ尖っているために、各突部9Aでの酸化膜のつき破り効果が高く、各突部9Aでの電気接触が極めて有効なものとなる。

【0013】なお、前記プローブ針8の先端面8cは、なるべくパッド1aの表面と平行になるように形成し、かつ前記突部9,9Aはそそれぞれの中心線がこの先端面8cと垂直な方向、すなわちプローブ針8とパッド1aとが接触される方向に突出されるように構成することが好ましい。この結果、各突部9,9Aの断面形状は、前記先端面8cに対して片側に偏位された鋸歯状に形成されることが好ましい。これにより、各突部9,9Aはパッド1aの表面に対して垂直方向に当接されることになり、酸化膜をより有効につき破り、好適な電気接触が可能となる。

【0014】なお、前記実施形態は本発明の一例を示したものであり、特にプローブ針の先端面に配設される突部の形状は配列形態は前記した構成以外に種々の構成が適用可能である。

## [0 0 1 5]

【発明の効果】以上説明したように本発明は、プローブ針の先端部が複数の突部で構成されているので、複数の突部のそれぞれにおいてパッドに電気接触されるため、接触面積を増大して、接触抵抗を低減することが可能となる。また、複数の突部がそれぞれパッド表面の酸化膜をつき破るため、好適な電気接触が可能となる。この場合、突部の形状として、先端が鋭角な三角状の断面形状をした経子状の突部として、あるいは、先端が鋭角な三角状の断面形状をした錘型の突部として形成することにより、前記酸化膜をより好適につき破ることが可能となり、さらに好適な電気接触状態を得ることができる。特に、突部の断面形状が鋸歯状であることができる。特に、突部の断面形状が鋸歯状であることにより、プ針がパッドに接触する際に各突部に加えられる当接力を有効に利用して前記酸化膜のつき破りを行うことが可能となる。

#### 【図面の簡単な説明】

【図1】本発明のプローブカードが適用されるテスタの要部の概略構成図である。

【図2】本発明にかかるプローブ針の先端部の拡大斜視図である。

【図3】図2のプローブ針とパッドとの接触状態を示す断面図である。

【図4】従来のプローブカードのプローブ針の先端部の 斜視図とその接触状態の断面図である。

【図5】従来の改善されたプローブカードにおけるプローブ針の先端部の斜視図とその接触状態の断面図である。

) 【図6】従来の他の改善されたプローブカードにおける

(4)

特開平11-51970

6

プローブ針の先端部の斜視図とその接触状態の断面図である。

5

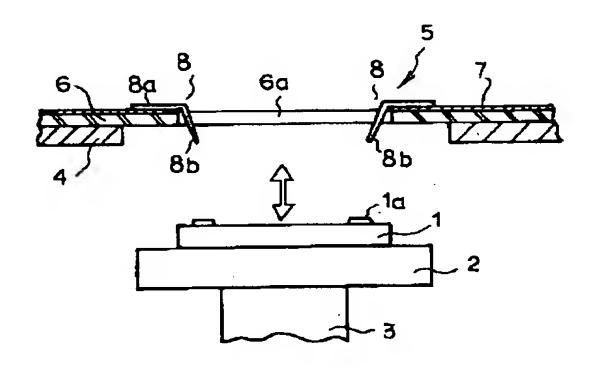
## 【符号の説明】

- 1 I C
- 2 ステージ
- 3 上下駆動機構
- 5 プローブカード

\* 6 絶縁基板

- 7 回路パターン
- 8 プローブ針
- 8 b 先端部
- 8 c 先端面
- 9,9A 突部

\*

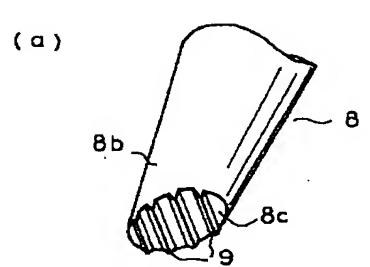


【図1】

1:10 (半導体集積回路装置)

- 1a:パッド
- 2:ステージ
- 3:上下驱動機構
- 4 : 固定ポード 5 : プローブカード
- 6: 絶縁基板
- 7:回路パターン 8:プローブ針
- Bb:先彎部

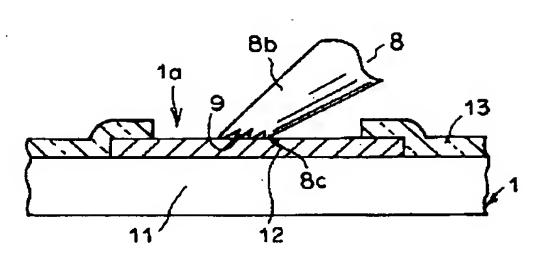
【図2】



8b 8c 8c

8:プローブ針 8b:先端部 8c:先端面 9:突部(格子状) 9A:突部(円錐状)

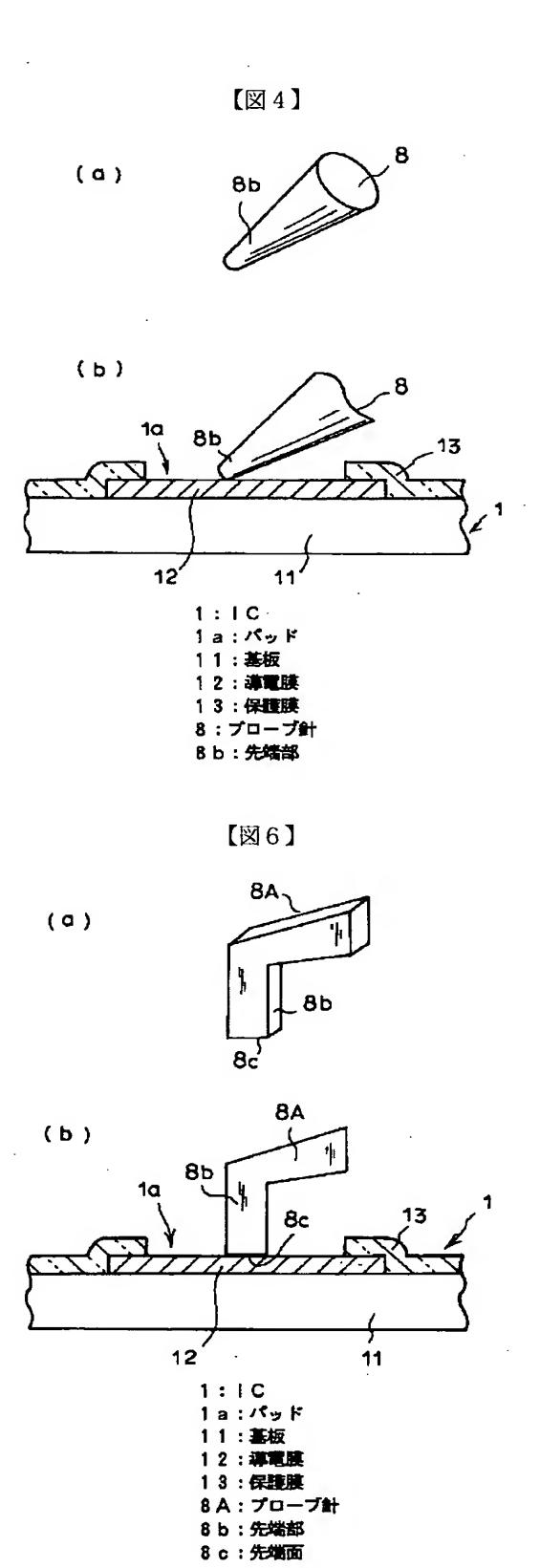
【図3】



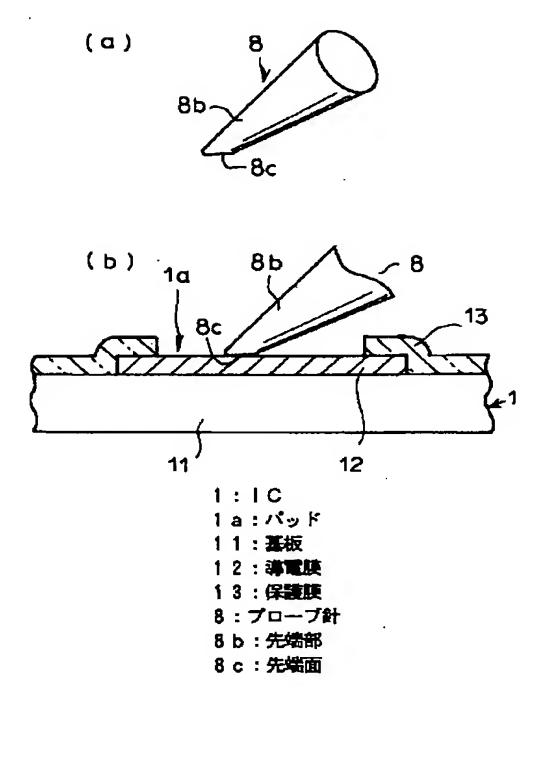
1:10

- 1a: パッド
- 11:基板
- 12:導電膜
- 13:保護膜
- 8:プローブ針

9:突部



10



【図5】

## PATENT ABSTRACTS OF JAPAN

(11)Publication number:

11-051970

(43) Date of publication of application: 26.02.1999

(51)Int.Cl.

GO1R 1/067

H01L 21/66

(21)Application number: 09-205709

(71)Applicant : NEC CORP

(22)Date of filing:

31.07.1997

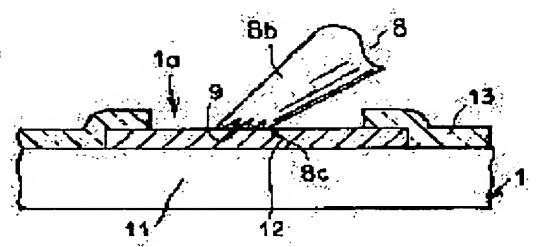
(72)Inventor: FUKUSHIMA YOSHIHIKO

### (54) PROBE CARD

## (57) Abstract:

PROBLEM TO BE SOLVED: To reduce the contact resistance, and to secure a good electrical contact in a probe needle which is electrically brought into contact with the pad of a semiconductor integrated circuit device for carrying out the measurement by means of a tester.

SOLUTION: In a probe card provided with a probe needle 8 which is electrically brought into contact with the pad 1c that has been provided to a semiconductor integrated circuit device 1 for electrically connecting the semiconductor integrated circuit device with a tester; the end part 8b of the probe needle 8 is made up of a plurality of projected parts 9, each of which is made up in a lattice-shaped projection whose end has an acute triangular shape in section, or in a weight-shaped projection whose end has an acute triangular shape in section. Since a plurality of projections 9 are electrically brought into contact with the pad 1a respectively, the contact surfaces are increased, and the contact



resistance can be reduced. Further, since a plurality of projections 9 break through the oxide film on the surface of time pad 1a respectively, a proper electrical contact can be enabled.

#### **LEGAL STATUS**

[Date of request for examination]

31.07.1997

[Date of sending the examiner's decision of

29.08.2000

rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision 2000-15425]

of rejection]

[Date of requesting appeal against examiner's 28.09.2000 decision of rejection] [Date of extinction of right]

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

## **CLAIMS**

## [Claim(s)]

[Claim 1] The probe card characterized by the point of said probe needle consisting of two or more projected parts in a probe card equipped with the probe needle for being contacted electrically to the pad prepared in semiconductor integrated circuit equipment, and carrying out electrical connection of said semiconductor integrated circuit equipment and circuit tester. [Claim 2] Said projected part is a probe card according to claim 1 which is the projected part of the shape of a grid which carried out the acute angle tip cross-section configuration of 3 corniform.

[Claim 3] Said projected part is a probe card according to claim 1 which is the spindle type projected part which carried out the acute angle tip cross-section configuration of 3 corniform. [Claim 4] Said projected part is a probe card according to claim 2 or 3 whose cross-section configuration is serrate.

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

#### **DETAILED DESCRIPTION**

[Detailed Description of the Invention] [0001]

[Field of the Invention] This invention relates to amelioration of the configuration of the point of a probe needle of contacting especially the bonding pad of a wafer, about the probe card used in case a wafer condition estimates a semiconductor device.

[0002]

[Description of the Prior Art] In order to measure the electrical characteristics of the semiconductor device formed on the semi-conductor wafer in the manufacture process of semiconductor integrated circuit equipment (IC) etc. generally, the test equipment called the so-called circuit tester is used. The probe card as a board for connecting a circuit tester body with the pad (electrode) of IC electrically is prepared, and it consists of this circuit tester so that the pad of IC may be contacted in a \*\*\*\*\* needle, i.e., a probe needle, in preparation for this probe card and electrical connection to IC may be performed. This probe card consists of fixed boards which carry out fixed support of the above mentioned probe needle and two or more probe needles.

[0003] The enlarged drawing of the probe needle point in the conventional probe card and the sectional view of a contact condition with the pad of IC are shown in <u>drawing 4</u> (a) and (b), respectively. Point 8b of the probe needle 8 is sharp, and the conventional probe card is carrying out the conic configuration. And point 8b of that probe needle 8 is contacted on the front face of pad 1a which consists of electric conduction film 12 which it was formed [ film ] in the front face of the silicon substrate 11 of IC1, and had the perimeter covered by the protective coat 13, and electrical connection is performed through this pad 1a. As such a probe card, the thing of a publication is in JP,62-295426,A and JP,4-199836,A, for example.

[0004] However, in such a probe card, since point 8b of the probe needle 8 is sharp, a touch area with pad 1a is small, and contact resistance tends to become large. For this reason, in carrying out conduction of the feeble current, a current falls by that contact resistance and there is a problem that suitable measurement becomes difficult. For this reason, in the former, as shown in drawing 5 (a) or drawing 6 (a), the tip of the probe needles 8 and 8A is formed in a flat surface, and the configuration in which this plane apical surface 8c is contacted to pad 1a is proposed. With these probe needles 8 and 8A, as shown in drawing 5 (b) or drawing 6 (b), it becomes possible to reduce the contact resistance which could increase and described above the touch area of pad 1a and the probe needle 8. As such a probe card, the thing of a publication is in JP,62-295426,A and JP,4-199836,A, for example:

[0005]

[Problem(s) to be Solved by the Invention] however, with the configuration formed in the flat surface, the tip of a probe needle in this way When the probe needles 8 and 8A contact the front face of pad 1a at a right include angle, it is good, but when a gap arises at the include angle Apical surface 8c of the probe needles 8 and 8A may be able to stop being able to contact the front face of pad 1a correctly, both may be unable to be in a line or a point contact condition as a result, and the above mentioned increase of a touch area and each effectiveness of reduction of contact resistance may be unable to be acquired, moreover, although this kind of probe needle

is generally considered also as the configuration which attaches and tears the about 100A thin oxide film generated on a pad front face, and is performing electrical connection, therefore sharpens the tip of a probe needle, as carried out, when [ said ] it forms in a flat surface, in question [ which \*\*s with it becoming impossible to attach and tear this thin oxide film, and \*\*\*\*\*\*\* suitable / \*\*\*\*\*\* / effect / becoming impossible ] \*\*\*\*s [ \*\*\*\*\*\*\*\* ] — \*\*. [0006] The purpose of this invention is to offer the probe card which reduced the contact resistance of a probe needle and a pad, enabled both suitable electric contact, and made it possible for the accuracy of measurement to improve and to improve the dependability of a test.

## [0007]

[Means for Solving the Problem] The probe card of this invention is characterized by the point of the probe needle contacted electrically to the pad of IC consisting of two or more projected parts. Here, said projected part is constituted as the projected part of the shape of a grid which carried out the acute angle tip cross—section configuration of 3 corniform, or a spindle type projected part which carried out the acute angle tip cross—section configuration of 3 corniform. Moreover, as for each projected part, it is desirable that the cross—section configuration is serrate.

[0008] Since it is contacted electrically by the pad in each of two or more projected parts, it becomes [increasing a touch area and reducing contact resistance or ] possible. Moreover, since two or more projected parts attach and tear the oxide film on the front face of a pad, respectively, suitable electric contact is attained.

[0009]

[Embodiment of the Invention] Next, the operation gestalt of this invention is explained with reference to a drawing. Drawing 1 is drawing showing the outline configuration of the principal part of a circuit tester equipped with the probe card of this invention. the vertical drive 3 which the silicon wafer 1 was laid in the stage 2 here [ IC and here ] where it is tested, and was formed in said stage 2 bottom — the upper and lower sides — it is supported movable. Moreover, above said stage 2, the fixed board 4 is arranged horizontally, and it is supported by the test mechanization outside drawing. A probe card 5 is supported by said fixed board 4. While opening of the rectangular window 6a is carried out to an insulating substrate 6, the necessary circuit pattern 7 is formed in that front face, and electrical connection of this probe card 5 is carried out to the test circuit outside drawing. And many probe needles 8 of a book are arranged in said rectangular window 6a, connection immobilization of the end face section 8a is carried out at said circuit pattern 7, the point 8b turns each probe needle 8 caudad through said rectangular window 6a, and it is projected. For this reason, by raising a stage 2, point 8b of each of said probe needle 8 is contacted to pad 1a prepared in IC1, and electrical connection of said IC1 and test circuit is carried out through this probe needle 8 and the circuit pattern 7, and it is constituted so that a necessary test may be performed.

[0010] Drawing 2 (a) is the expansion perspective view of point 8b of said probe needle 8. Although, as for point 8b of this probe needle 8, the whole configuration is formed in the cone mold, while that apical surface 8c is formed in the field in the condition of having inclined to the shaft orientations of a probe needle, array formation of two or more projected parts 9 to which the cross-section configuration made [ the tip ] the triangle this apical surface by the acute angle is carried out at the shape of a grid. Or as shown in drawing 2 (b), it is good for said apical surface 8c also as a configuration whose tip is an acute angle and which arranged regularly or irregularly two or more projection 9A of the minute cone mold of an individual. In addition, the diameter is formed for said apical surface 8c as the round shape thru/or ellipse form below 80microphi here. Moreover, projected part 9A of drawing 2 (b) may be a \*\*\*\* type. [0011] Drawing 3 is the expanded sectional view of the important section in the condition of testing IC1 with the above mentioned probe card 5. The electric conduction film 12 of the necessary pattern formed in the front face of the substrate 11 of a silicon wafer is covered with the protective coat 13, said IC1 is exposed by opening prepared in this protective coat 13, and said pad 1a is formed with said electric conduction film 12. And as shown in drawing 1, when a stage 2 goes up and pad 1a is contacted at the tip of the probe needle 8, electric contact to the probe needle 8 as shown in <u>drawing 3</u>, and pad 1a is performed. Since the acute angle tip projected part 9 is arranged in apical surface 8c of the probe needle 8 at this time, the oxide film outside drawing generated on the front face of pad 1a in the tip of these projected parts 9 will be attached and torn, and it will be contacted electrically by the electric conduction film 12 of pad 1a on it. Therefore, since the probe needle 8 and pad 1a are contacted electrically in two or more projected parts 9, respectively, they increase a touch area conventionally which is contacted by one sharp point as compared with a configuration, and become possible [ reducing contact resistance ]. Moreover, since the oxide film on the front face of a pad is attached and torn at each tip of two or more projected parts 9 by one of these, good electric contact can be obtained.

[0012] Especially, with the probe needle 8 of the configuration of <u>drawing 2</u> (a), since the projected part 9 is formed in the shape of a grid, since each projected part 9 and pad 1a are made into a line contact condition, they can reduce contact resistance rather than the configuration of projected part 9A of the letter of a projection which each of the configuration of <u>drawing 2</u> (b) became independent of. Moreover, by one side, with the configuration of <u>drawing 2</u> (b), since each projected part 9A is sharp, respectively, the oxide film in each projected part 9A sticks, and it breaks, and effectiveness is high and becomes what has the very effective electric contact by each projected part 9A.

[0013] in addition, form apical surface 8c of said probe needle 8 to become the front face of pad 1a, and parallel if possible, and excite said projected parts 9 and 9A — it is desirable to constitute so that it may be projected and carried out in the direction where the center line of \*\*\*\* is perpendicular to this apical surface 8c, i.e., the direction in which the probe needle 8 and pad 1a are contacted. Consequently, as for the cross—section configuration of each projected parts 9 and 9A, it is desirable to be formed in serrate [ which was biased by one side to said apical surface 8c]. Thereby, each projected parts 9 and 9A will be perpendicularly contacted to the front face of pad 1a, attach an oxide film more effectively, and tear it, and the suitable electric contact of them is attained.

[0014] In addition, said operation gestalt can show an example of this invention, and the configuration of the projected part arranged especially in the apical surface of a probe needle can apply various configurations in addition to the configuration which described the array gestalt above.

[0015]

[Effect of the Invention] As explained above, since the point of a probe needle consists of two or more projected parts and this invention is contacted electrically by the pad in each of two or more projected parts, it increases a touch area and becomes possible [reducing contact resistance]. Moreover, since two or more projected parts attach and tear the oxide film on the front face of a pad, respectively, suitable electric contact is attained. In this case, by forming as the projected part of the shape of a grid which carried out the acute angle tip cross-section configuration of 3 corniform as a configuration of a projected part, or a spindle type projected part which carried out the acute angle tip cross-section configuration of 3 corniform, it becomes possible to attach said oxide film more suitably and to tear it, and a still more suitable electric contact condition can be acquired. Especially, when the cross-section configuration of a projected part is serrate, in case a probe needle contacts a pad, it becomes possible for said oxide film to stick using effectively the contact force applied to each projected part, and to perform \*\*\*\*.

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

### **TECHNICAL FIELD**

[Field of the Invention] This invention relates to amelioration of the configuration of the point of a probe needle of contacting especially the bonding pad of a wafer, about the probe card used in case a wafer condition estimates a semiconductor device.

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

#### **PRIOR ART**

[Description of the Prior Art] In order to measure the electrical characteristics of the semiconductor device formed on the semi-conductor wafer in the manufacture process of semiconductor integrated circuit equipment (IC) etc. generally, the test equipment called the so-called circuit tester is used. The probe card as a board for connecting a circuit tester body with the pad (electrode) of IC electrically is prepared, and it consists of this circuit tester so that the pad of IC may be contacted in a \*\*\*\*\*\* needle, i.e., a probe needle, in preparation for this probe card and electrical connection to IC may be performed. This probe card consists of fixed boards which carry out fixed support of the above mentioned probe needle and two or more probe needles.

[0003] The enlarged drawing of the probe needle point in the conventional probe card and the sectional view of a contact condition with the pad of IC are shown in <u>drawing 4</u> (a) and (b), respectively. Point 8b of the probe needle 8 is sharp, and the conventional probe card is carrying out the conic configuration. And point 8b of that probe needle 8 is contacted on the front face of pad 1a which consists of electric conduction film 12 which it was formed [ film ] in the front face of the silicon substrate 11 of IC1, and had the perimeter covered by the protective coat 13, and electrical connection is performed through this pad 1a. As such a probe card, the thing of a publication is in JP,62-295426,A and JP,4-199836,A, for example.

[0004] However, in such a probe card, since point 8b of the probe needle 8 is sharp, a touch area with pad 1a is small, and contact resistance tends to become large. For this reason, in carrying out conduction of the feeble current, a current falls by that contact resistance and there is a problem that suitable measurement becomes difficult. For this reason, in the former, as shown in drawing 5 (a) or drawing 6 (a), the tip of the probe needles 8 and 8A is formed in a flat surface, and the configuration in which this plane apical surface 8c is contacted to pad 1a is proposed. With these probe needles 8 and 8A, as shown in drawing 5 (b) or drawing 6 (b), it becomes possible to reduce the contact resistance which could increase and described above the touch area of pad 1a and the probe needle 8. As such a probe card, the thing of a publication is in JP,62-295426,A and JP,4-199836,A, for example.

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

#### **EFFECT OF THE INVENTION**

[Effect of the Invention] As explained above, since the point of a probe needle consists of two or more projected parts and this invention is contacted electrically by the pad in each of two or more projected parts, it increases a touch area and becomes possible [ reducing contact resistance ]. Moreover, since two or more projected parts attach and tear the oxide film on the front face of a pad, respectively, suitable electric contact is attained. In this case, by forming as the projected part of the shape of a grid which carried out the acute angle tip cross—section configuration of 3 corniform as a configuration of a projected part, or a spindle type projected part which carried out the acute angle tip cross—section configuration of 3 corniform, it becomes possible to attach said oxide film more suitably and to tear it, and a still more suitable electric contact condition can be acquired. Especially, when the cross—section configuration of a projected part is serrate, in case a probe needle contacts a pad, it becomes possible for said oxide film to stick using effectively the contact force applied to each projected part, and to perform \*\*\*\*.

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

#### **TECHNICAL PROBLEM**

[Problem(s) to be Solved by the Invention] however, with the configuration formed in the flat surface, the tip of a probe needle in this way When the probe needles 8 and 8A contact the front face of pad 1a at a right include angle, it is good, but when a gap arises at the include angle Apical surface 8c of the probe needles 8 and 8A may be able to stop being able to contact the front face of pad 1a correctly, both may be unable to be in a line or a point contact condition as a result, and the above mentioned increase of a touch area and each effectiveness of reduction of contact resistance may be unable to be acquired. moreover, although this kind of probe needle is generally considered also as the configuration which attaches and tears the about 100A thin oxide film generated on a pad front face, and is performing electrical connection, therefore sharpens the tip of a probe needle, as carried out, when [ said ] it forms in a flat surface, in question [ which \*\*s with it becoming impossible to attach and tear this thin oxide film, and \*\*\*\*\*\*\*\* suitable / \*\*\*\*\*\* / effect / becoming impossible ] \*\*\*\*\*s [ \*\*\*\*\*\*\*\* ] — \*\*.

[0006] The purpose of this invention is to offer the probe card which reduced the contact resistance of a probe needle and a pad, enabled both suitable electric contact, and made it possible for the accuracy of measurement to improve and to improve the dependability of a test.

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

#### **MEANS**

[Means for Solving the Problem] The probe card of this invention is characterized by the point of the probe needle contacted electrically to the pad of IC consisting of two or more projected parts. Here, said projected part is constituted as the projected part of the shape of a grid which carried out the acute angle tip cross—section configuration of 3 corniform, or a spindle type projected part which carried out the acute angle tip cross—section configuration of 3 corniform. Moreover, as for each projected part, it is desirable that the cross—section configuration is serrate.

[0008] Since it is contacted electrically by the pad in each of two or more projected parts, it becomes [increasing a touch area and reducing contact resistance or ] possible. Moreover, since two or more projected parts attach and tear the oxide film on the front face of a pad, respectively, suitable electric contact is attained.

[0009]

[Embodiment of the Invention] Next, the operation gestalt of this invention is explained with reference to a drawing. Drawing 1 is drawing showing the outline configuration of the principal part of a circuit tester equipped with the probe card of this invention. the vertical drive 3 which the silicon wafer 1 was laid in the stage 2 here [ IC and here ] where it is tested, and was formed in said stage 2 bottom — the upper and lower sides — it is supported movable. Moreover, above said stage 2, the fixed board 4 is arranged horizontally, and it is supported by the test mechanization outside drawing. A probe card 5 is supported by said fixed board 4. While opening of the rectangular window 6a is carried out to an insulating substrate 6, the necessary circuit pattern 7 is formed in that front face, and electrical connection of this probe card 5 is carried out to the test circuit outside drawing. And many probe needles 8 of a book are arranged in said rectangular window 6a, connection immobilization of the end face section 8a is carried out at said circuit pattern 7, the point 8b turns each probe needle 8 caudad through said rectangular window 6a, and it is projected. For this reason, by raising a stage 2, point 8b of each of said probe needle 8 is contacted to pad 1a prepared in IC1, and electrical connection of said IC1 and test circuit is carried out through this probe needle 8 and the circuit pattern 7, and it is constituted so that a necessary test may be performed.

[0010] <u>Drawing 2</u> (a) is the expansion perspective view of point 8b of said probe needle 8. Although, as for point 8b of this probe needle 8, the whole configuration is formed in the cone mold, while that apical surface 8c is formed in the field in the condition of having inclined to the shaft orientations of a probe needle, array formation of two or more projected parts 9 to which the cross-section configuration made [ the tip ] the triangle this apical surface by the acute angle is carried out at the shape of a grid. Or as shown in <u>drawing 2</u> (b), it is good for said apical surface 8c also as a configuration whose tip is an acute angle and which arranged regularly or irregularly two or more projection 9A of the minute cone mold of an individual. In addition, the diameter is formed for said apical surface 8c as the round shape thru/or ellipse form below 80microphi here. Moreover, projected part 9A of <u>drawing 2</u> (b) may be a \*\*\*\* type.

[0011] <u>Drawing 3</u> is the expanded sectional view of the important section in the condition of testing IC1 with the above mentioned probe card 5. The electric conduction film 12 of the

necessary pattern formed in the front face of the substrate 11 of a silicon wafer is covered with

the protective coat 13, said IC1 is exposed by opening prepared in this protective coat 13, and said pad 1a is formed with said electric conduction film 12. And as shown in <u>drawing 1</u>, when a stage 2 goes up and pad 1a is contacted at the tip of the probe needle 8, electric contact to the probe needle 8 as shown in <u>drawing 3</u>, and pad 1a is performed. Since the acute angle tip projected part 9 is arranged in apical surface 8c of the probe needle 8 at this time, the oxide film outside drawing generated on the front face of pad 1a in the tip of these projected parts 9 will be attached and torn, and it will be contacted electrically by the electric conduction film 12 of pad 1a on it. Therefore, since the probe needle 8 and pad 1a are contacted electrically in two or more projected parts 9, respectively, they increase a touch area conventionally which is contacted by one sharp point as compared with a configuration, and become possible [ reducing contact resistance ]. Moreover, since the oxide film on the front face of a pad is attached and torn at each tip of two or more projected parts 9 by one of these, good electric contact can be obtained.

[0012] Especially, with the probe needle 8 of the configuration of <u>drawing 2</u> (a), since the projected part 9 is formed in the shape of a grid, since each projected part 9 and pad 1a are made into a line contact condition, they can reduce contact resistance rather than the configuration of projected part 9A of the letter of a projection which each of the configuration of <u>drawing 2</u> (b) became independent of. Moreover, by one side, with the configuration of <u>drawing 2</u> (b), since each projected part 9A is sharp, respectively, the oxide film in each projected part 9A sticks, and it breaks, and effectiveness is high and becomes what has the very effective electric contact by each projected part 9A.

[0013] in addition, form apical surface 8c of said probe needle 8 to become the front face of pad 1a, and parallel if possible, and excite said projected parts 9 and 9A — it is desirable to constitute so that it may be projected and carried out in the direction where the center line of \*\*\*\* is perpendicular to this apical surface 8c, i.e., the direction in which the probe needle 8 and pad 1a are contacted. Consequently, as for the cross-section configuration of each projected parts 9 and 9A, it is desirable to be formed in serrate [ which was biased by one side to said apical surface 8c]. Thereby, each projected parts 9 and 9A will be perpendicularly contacted to the front face of pad 1a, attach an oxide film more effectively, and tear it, and the suitable electric contact of them is attained.

[0014] In addition, said operation gestalt can show an example of this invention, and the configuration of the projected part arranged especially in the apical surface of a probe needle can apply various configurations in addition to the configuration which described the array gestalt above.

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

### **DESCRIPTION OF DRAWINGS**

[Brief Description of the Drawings]

[Drawing 1] It is the outline block diagram of the important section of a circuit tester with which the probe card of this invention is applied.

[Drawing 2] It is the expansion perspective view of the point of the probe needle concerning this invention.

[Drawing 3] It is the sectional view showing the contact condition of the probe needle of <u>drawing</u> 2, and a pad.

[Drawing 4] It is the perspective view and the sectional view of a contact condition of the point of the probe needle of the conventional probe card.

[Drawing 5] It is the perspective view and the sectional view of a contact condition of the point of the probe needle in the probe card with which the former has been improved.

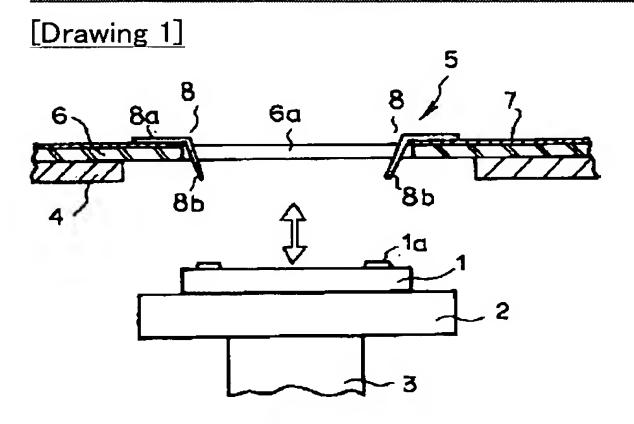
[Drawing 6] It is the perspective view and the sectional view of a contact condition of the point of the probe needle in the probe card with which everything but the former has been improved. [Description of Notations]

- 1 IC
- 2 Stage
- 3 Vertical Drive
- 5 Probe Card
- 6 Insulating Substrate
- 7 Circuit Pattern
- 8 Probe Needle
- 8b Point
- 8c Apical surface
- 9 9A Projected part

JPO and NCIPI are not responsible for any damages caused by the use of this translation.

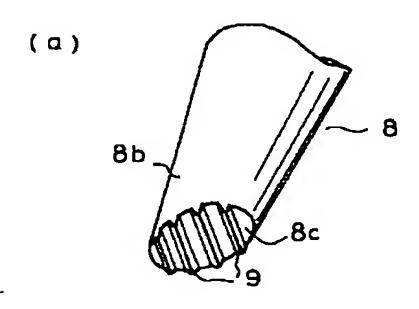
- 1. This document has been translated by computer. So the translation may not reflect the original precisely.
- 2.\*\*\*\* shows the word which can not be translated.
- 3.In the drawings, any words are not translated.

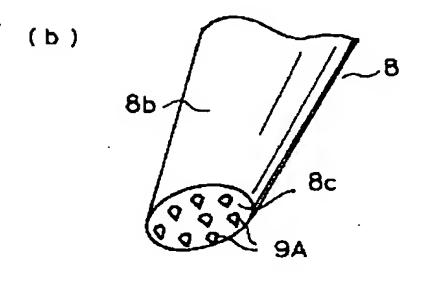
## **DRAWINGS**



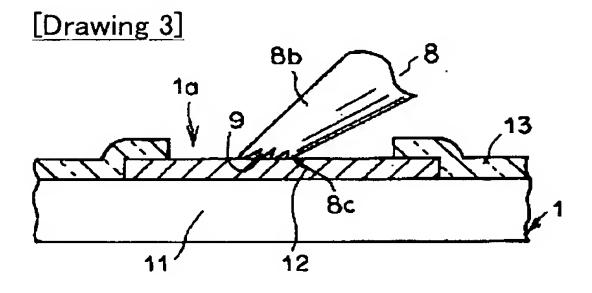
- 1:1C(半導体集積回路装置)
- 1a:パッド
- 2:ステージ
- 3:上下驱動機構
- 4:固定ポード
- 5:プローブカード
- 6: 絶縁基板
- 7:回路パターン
- B:プローブ針
- B b:先端部

## [Drawing 2]



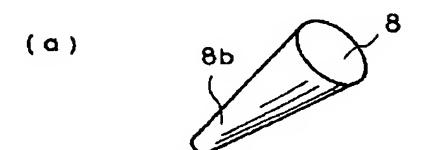


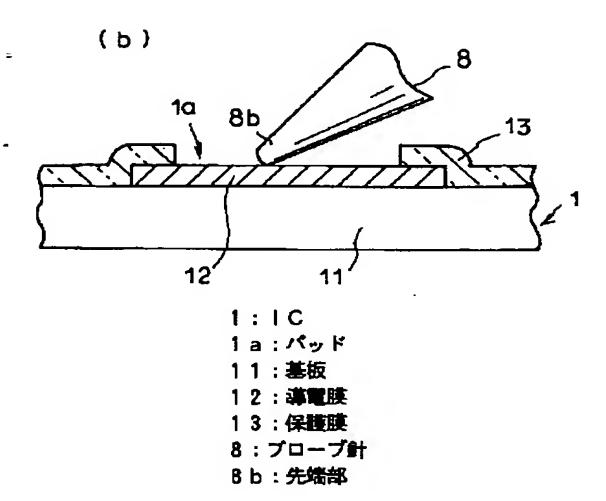
8:プローブ針 8b:先端部 8c:先端面 9:突部(格子状) 9A:突部(円錐状)

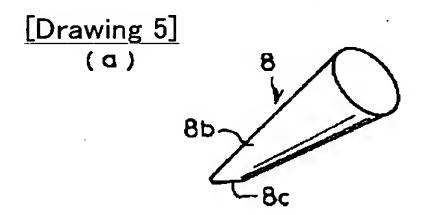


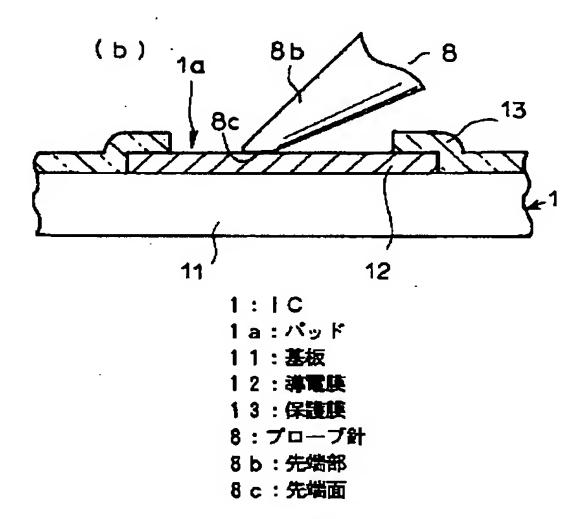
1:IC 1a:パッド 11:基板 12:導電膜 13:保護膜 8:プローブ針 9:突部

# [Drawing 4]









# [Drawing 6]

